(714) 557-3800 Blakely, Sokoloff, Taylor & Zafman LLP Title: Branch-Aware FIFO for Interprocessor Data Sharing

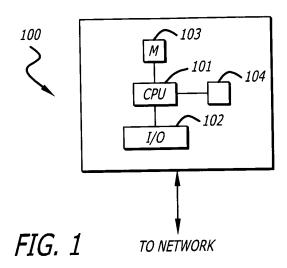
1st Named Inventor: Jose S. Niell

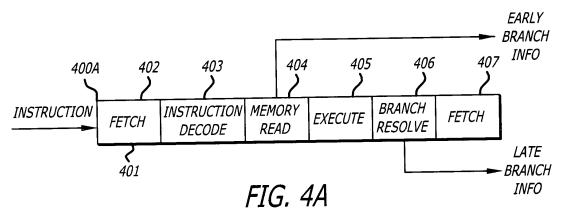
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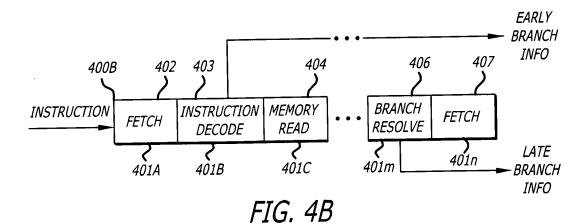
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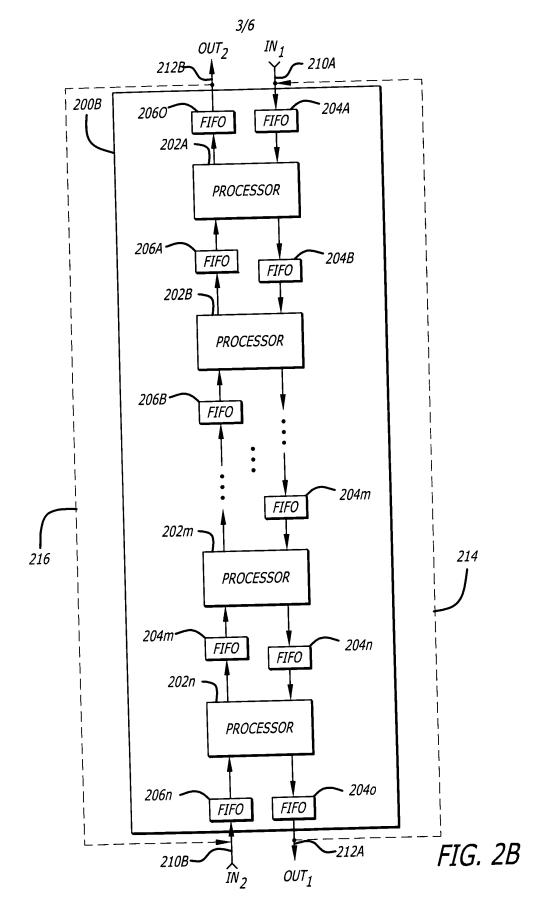
2/6 210-200A 204A FIF0 202A **PROCESSOR** 204B FIF0 202B · **PROCESSOR** 204m · FIF0 202m -**PROCESSOR** 214 204n FIF0 202n **PROCESSOR** 2040 -FIF0 FIG. 2A 212-OUT

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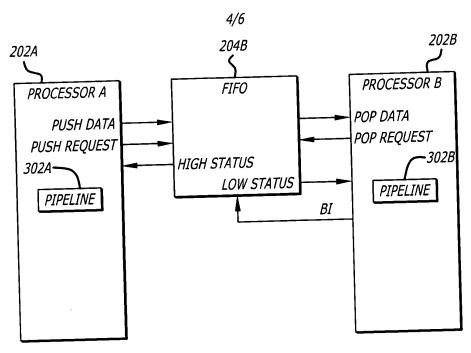


FIG. 3A

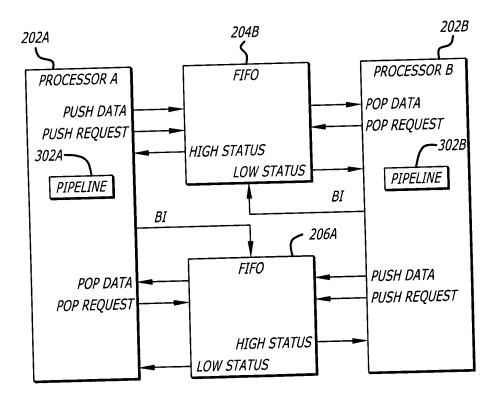


FIG. 3B

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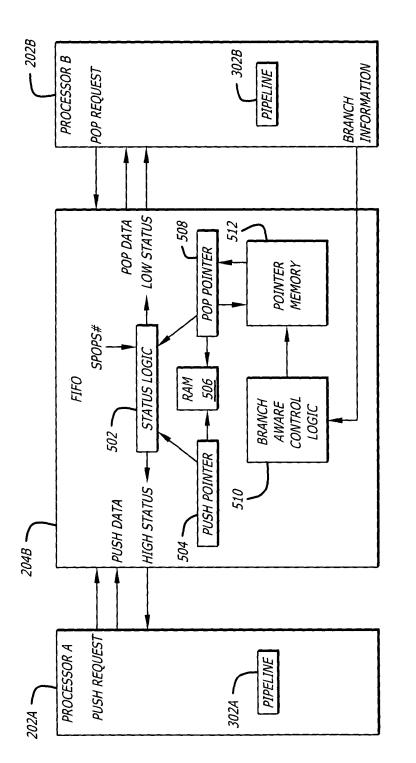


FIG. 54

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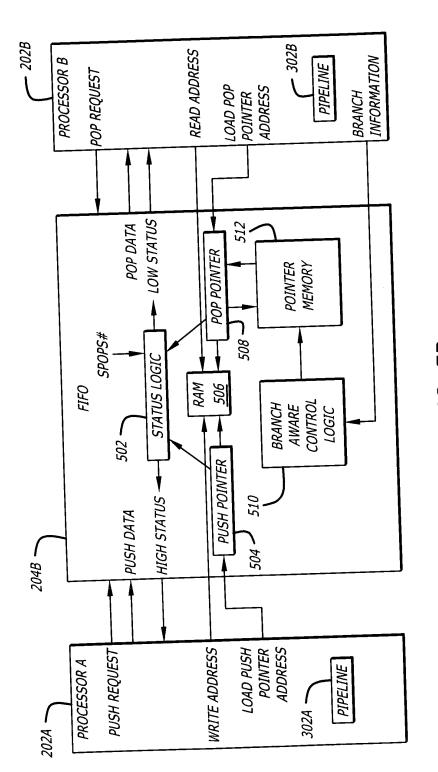


FIG. 5B